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means meeting system level objectives by exploiting the synergism of hardware and software through their concurrent design. Co-design problems have different flavors according to the application domain,

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implementing technology
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Digital hardware design has
increasingly more
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parallelizing programming
environment CoDe-X

introducing

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strategies on two levels of

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partitionings for data-driven
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Hardware/Software Co-Design
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ABSTRACT

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(FPGAs), including hardware
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software (in C) and hardware
(in VHDL) is generated from
a single program, along with

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the code to support
communication between
hardware and software. We
present type-based
techniques for the
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2004. Stockholm, Sweden.
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Bolsens, I.; Hugo J. de Man
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Design of Digital
Telecommunication Systems.
Proceedings of the IEEE,
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are critical research topics
for embedded system design.
Hardware-software co-design
of embedded systems must be
performed at several

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different levels of abstraction, but the highest levels of abstraction in co-design are more abstract than the typical software coder or ASIC designer may be used to.

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