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The transistor is the key enabler of modern electronics. Progress in transistor scaling has pushed channel lengths to the nanometer regime where traditional approaches to device physics are less and less suitable. These lectures describe a way of understanding MOSFETs and other transistors that is much more suitable than traditional approaches when the critical dimensions are measured in nanometers. It uses a novel, “bottom-up approach” that agrees with traditional methods when devices are large, but that also works for nano-devices. Surprisingly, the final result looks much like the traditional, textbook, transistor models, but the parameters in the equations have simple, clear interpretations at the nanoscale. The objective is to provide readers with an understanding of the essential physics of nanoscale transistors as well as some of the practical technological considerations and fundamental limits. This book is written in a way that is broadly accessible to students with only a very basic knowledge of semiconductor physics and electronic circuits. Complemented with online lecture by Prof Lundstrom: nanoHUB-U Nanoscale Transistor Contents:MOSFET Fundamentals:OverviewThe Transistor as a Black BoxThe MOSFET: A Barrier-Controlled DeviceMOSFET IV: Traditional ApproachMOSFET IV: The Virtual Source ModelMOS Electrostatics:Poisson Equation and the Depletion ApproximationGate Voltage and Surface PotentialMobile Charge: Bulk MOSMobile Charge: Extremely Thin SOI2D MOS ElectrostaticsThe VS Model RevisitedThe Ballistic MOSFET:The Landauer Approach to TransportThe Ballistic MOSFETThe Ballistic Injection VelocityConnecting the Ballistic and VS ModelsTransmission Theory of the MOSFET:Carrier Scattering and TransmissionTransmission Theory of the MOSFETConnecting the Transmission and VS ModelsVS Characterization of Transport in NanotransistorsLimits and Limitations Readership: Any student and professional with an undergraduate degree in the physical sciences or engineering.

The Number 1 VLSI Design Guide—Now Fully Updated for IP-Based Design and the Newest Technologies Modern VLSI Design, Fourth Edition, offers authoritative, up-to-the-minute guidance for the entire VLSI design process—from architecture and logic design through layout and packaging. Wayne Wolf has systematically updated his award-winning book for today’s newest technologies and highest-value design techniques. Wolf introduces powerful new IP-based design techniques at all three levels: gates, subsystems, and architecture. He presents deeper coverage of logic design fundamentals, clocking and timing, and much more. No other VLSI guide presents as much up-to-date information for maximizing performance, minimizing power utilization, and achieving rapid design turnarounds.

The five-volume set may serve as a comprehensive reference on electromagnetic analysis and its applications at all frequencies, from static fields to optics and photonics. The material includes micro- and nanomagnetics, the new generation of electric machines, renewable energy, hybrid vehicles, low-noise motors; antennas and microwave devices, plasmonics, metamaterials, lasers, and more.Written at a level accessible to both graduate students and engineers, Electromagnetic Analysis is a comprehensive reference, covering methods and applications at all frequencies (from statics to optical). Each volume contains pedagogical/tutorial material of high archival value as well as chapters on state-of-the-art developments.

Machine learning is a potential solution to resolve bottleneck issues in VLSI via optimizing tasks in the design process. This book aims to provide the latest machine-learning–based methods, algorithms, architectures, and frameworks designed for VLSI design. The focus is on digital, analog, and mixed-signal design techniques, device modeling, physical design, hardware implementation, testability, reconfigurable design, synthesis and verification, and related areas. Chapters include case studies as well as novel research ideas in the given field. Overall, the book provides practical implementations of VLSI design, IC design, and hardware realization using machine learning techniques. Features: Provides the details of state-of-the-art machine learning methods used in VLSI design Discusses hardware implementation and device modeling pertaining to machine learning algorithms Explores machine learning for various VLSI architectures and reconfigurable computing Illustrates the latest techniques for device size and feature optimization Highlights the latest case studies and reviews of the methods used for hardware implementation This book is aimed at researchers, professionals, and graduate students in VLSI, machine learning, electrical and electronic engineering, computer engineering, and hardware systems.

The book is designed as an introduction for engineers and researchers wishing to obtain a fundamental knowledge and a snapshot in time of the cutting edge in technology research. As a natural consequence, Nano and Giga Challenges is also an essential reference for the "gurus" wishing to keep abreast of the latest directions and challenges in microelectronic technology development and future trends. The combination of viewpoints presented within the book can help to foster further research and cross-disciplinary interaction needed to surmount the barriers facing future generations of technology design. Key Features: • Quickly becoming the hottest topic of the new millennium (2.4 billion dollars funding in US alone • Current status and future trends of micro and nanoelectronics research • Written by leading experts in the corresponding research areas • Excellent tutorial for graduate students and reference for "gurus"

The book comprises select proceedings of the first International Conference on Advances in Electrical and Computer Technologies 2019 (ICAECT 2019). The papers presented in this book are peer reviewed and cover wide range of topics in Electrical and Computer Engineering fields. This book contains the papers presenting the latest developments in the areas of Electrical, Electronics, Communication systems and Computer Science such as smart grids, soft computing techniques in power systems, smart energy management systems, power electronics, feedback control systems, biomedical engineering, geo informative systems, grid computing, data mining, image and signal processing, video processing, computer vision, pattern recognition, cloud computing, pervasive computing, intelligent systems, artificial intelligence, neural network and fuzzy logic, broad band communication, mobile and optical communication, network security, VLSI, embedded systems, optical networks and wireless communication. This book will be of great use to the researchers and students in the areas of Electrical and Electronics Engineering, Communication systems and Computer Science.

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